SPEC® CFP2006 Result

ASUS Computer International

(Test Sponsor: Intel Corporation)

Asus G2S (Intel Core 2 Duo T7700)

$SPECfp®2006 =$ 14.5

$SPECfp\_base2006 =$ 14.0

CPU2006 license: 13	**Test date:**	Jun-2007
Test sponsor: Intel Corporation	**Hardware Availability:**	Jun-2007
Tested by: Intel Corporation	**Software Availability:**	May-2007

Results graph (scale 0 to 23.0):

Benchmark	Peak	Base
410.bwaves	19.8	19.9
416.gamess	13.1	12.9
433.milc	9.73	9.71
434.zeusmp	14.9	13.9
435.gromacs	14.0	13.8
436.cactusADM		16.0
437.leslie3d	12.4	12.4
444.namd	12.4	12.4
447.dealII	18.8	16.9
450.soplex	12.7	12.8
453.povray	22.0	17.2
454.calculix	10.9	10.9
459.GemsFDTD	10.6	
465.tonto	13.4	13.0
470.lbm	15.1	14.2
481.wrf	16.4	16.4
482.sphinx3	20.9	20.8

SPECfp_base2006 = 14.0

SPECfp2006 = 14.5

Hardware

CPU Name:	Intel Core 2 Duo T7700
CPU Characteristics:	2.40 GHz, 4MB L2, 800 MHz Bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

Continued on next page

Software

Operating System:	Windows Vista32 Ultimate
Compiler:	Intel C++ Compiler for IA32 version 10.0
	Build 20070426 Package ID: W_CC_P_10.0.025
	Intel Fortran Compiler for IA32 version 10.0
	Build 20070426 Package ID: W_FC_P_10.0.025
	Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default

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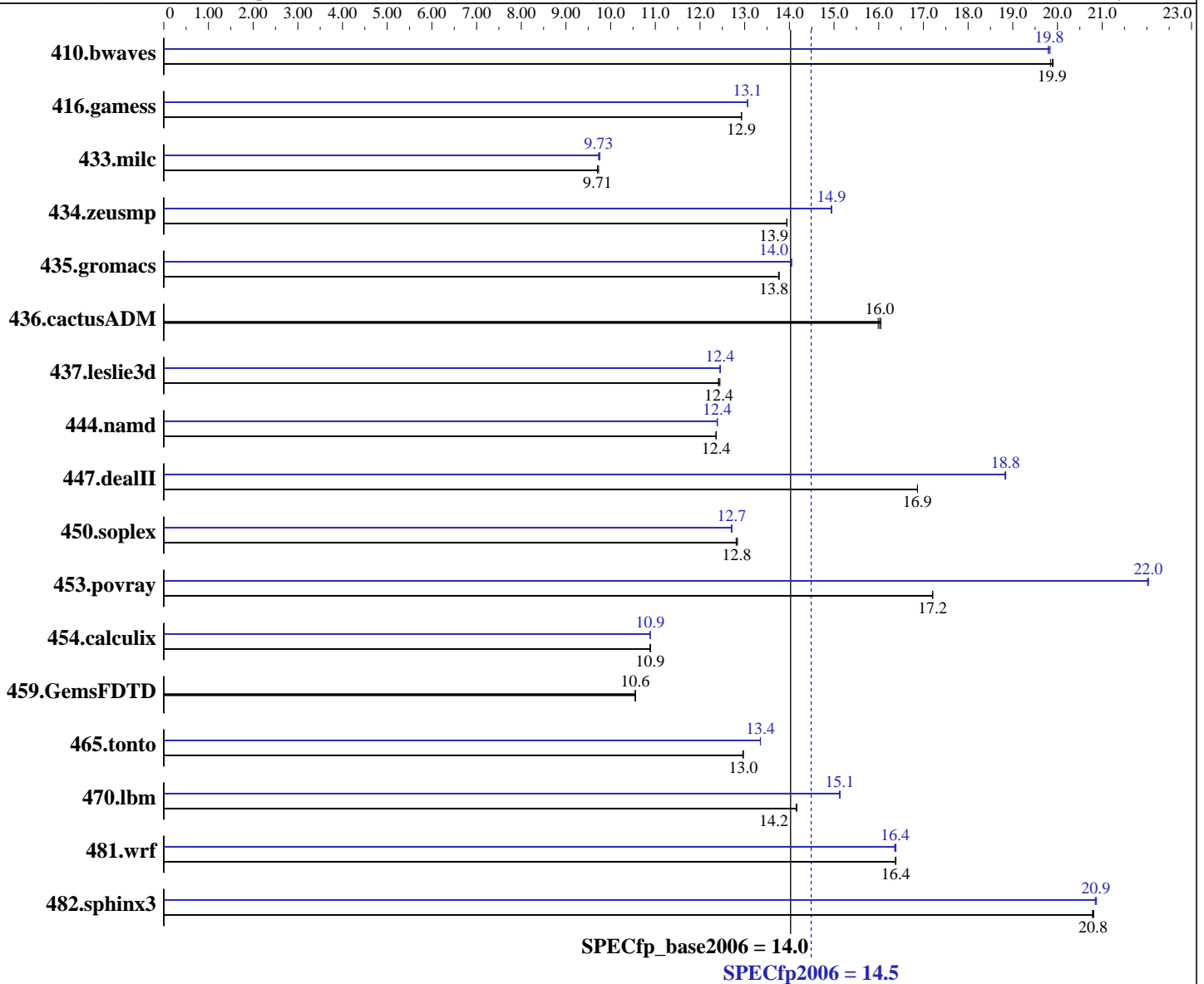| SPECfp2006 =	14.5
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L3 Cache:	None
Other Cache:	None
Memory:	2 GB (2x1GB Hynix DDR2-667 CL5)
Disk Subsystem:	160GB Hitachi SATA, 5400RPM
Other Hardware:	None

Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	None
	SmartHeap Library Version 8.0 from
	http://www.microquill.com/

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	683	19.9	**684**	**19.9**	685	19.8	685	19.8	687	19.8	**686**	**19.8**
416.gamess	**1515**	**12.9**	1514	12.9	1515	12.9	1499	13.1	**1499**	**13.1**	1499	13.1
433.milc	946	9.70	**946**	**9.71**	943	9.73	943	9.73	**943**	**9.73**	941	9.76
434.zeusmp	**653**	**13.9**	653	13.9	653	13.9	609	14.9	**609**	**14.9**	609	14.9
435.gromacs	519	13.8	**518**	**13.8**	518	13.8	508	14.1	509	14.0	**508**	**14.0**
436.cactusADM	745	16.0	**746**	**16.0**	747	16.0	745	16.0	**746**	**16.0**	747	16.0
437.leslie3d	755	12.4	**757**	**12.4**	757	12.4	755	12.5	755	12.4	**755**	**12.4**
444.namd	649	12.4	**649**	**12.4**	649	12.4	648	12.4	647	12.4	**648**	**12.4**
447.dealII	678	16.9	678	16.9	**678**	**16.9**	608	18.8	**607**	**18.8**	607	18.8
450.soplex	**650**	**12.8**	650	12.8	651	12.8	657	12.7	656	12.7	**656**	**12.7**
453.povray	309	17.2	**309**	**17.2**	309	17.2	241	22.0	**241**	**22.0**	242	22.0
454.calculix	757	10.9	**758**	**10.9**	758	10.9	758	10.9	758	10.9	**758**	**10.9**
459.GemsFDTD	**1006**	**10.6**	1005	10.6	1006	10.6	**1006**	**10.6**	1005	10.6	1006	10.6
465.tonto	759	13.0	759	13.0	**759**	**13.0**	**737**	**13.4**	737	13.4	737	13.4
470.lbm	971	14.2	970	14.2	**971**	**14.2**	908	15.1	908	15.1	**908**	**15.1**
481.wrf	682	16.4	**682**	**16.4**	682	16.4	682	16.4	683	16.4	**682**	**16.4**
482.sphinx3	938	20.8	**937**	**20.8**	936	20.8	934	20.9	**934**	**20.9**	935	20.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
The system bus runs at 667 MHz
System was configured with an nVIDIA 8600M GT graphics card
Binaries were built on Windows XP Professional SP2
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

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Hardware Availability: Jun-2007
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Base Compiler Invocation (Continued)

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
     444.namd: -TP
    447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
                -DBOOST_NO_INTRINSIC_WCHAR_T
    453.povray: -DSPEC_CPU_WINDOWS_ICL
   454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
       481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:
```
-fast /F950000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F950000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Fortran benchmarks:
```
-fast /F950000000
```

Benchmarks using both Fortran and C:
```
-fast /F950000000
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

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Hardware Availability: Jun-2007
Software Availability: May-2007

Peak Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore
   444.namd: -TP
  447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
              -DBOOST_NO_INTRINSIC_WCHAR_T
  453.povray: -DSPEC_CPU_WINDOWS_ICL
 454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
     481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Peak Optimization Flags

C benchmarks:

```
   433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Oa
             /F950000000 shlw32m.lib           -link /FORCE:MULTIPLE

   470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
            -Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib
                       -link /FORCE:MULTIPLE

 482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
              /F950000000 shlw32m.lib           -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
   444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
             -Qcxx_features /F950000000 shlw32m.lib
                       -link /FORCE:MULTIPLE

  447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
              -Qcxx_features /F950000000 shlw32m.lib
                       -link /FORCE:MULTIPLE

  450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
              /F950000000 shlw32m.lib           -link /FORCE:MULTIPLE

  453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
              -Qcxx_features /F950000000 shlw32m.lib
                       -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
  410.bwaves: -fast /F950000000

  416.gamess: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Ob0
              -Qansi-alias -Qscalar-rep- /F950000000

  434.zeusmp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qprec_div-
              -Qunroll0 -Qscalar-rep- /F950000000
```

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Peak Optimization Flags (Continued)

437.leslie3d: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa`
`/F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml

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