



# SPEC<sup>®</sup> CFP2006 Result

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Dell Inc.  
(Test Sponsor: Intel Corporation)

SPECfp<sup>®</sup>\_rate2006 = 16.2

Dell XPS M1710 (Intel Core Duo T2600)

SPECfp\_rate\_base2006 = 15.9

CPU2006 license: 13

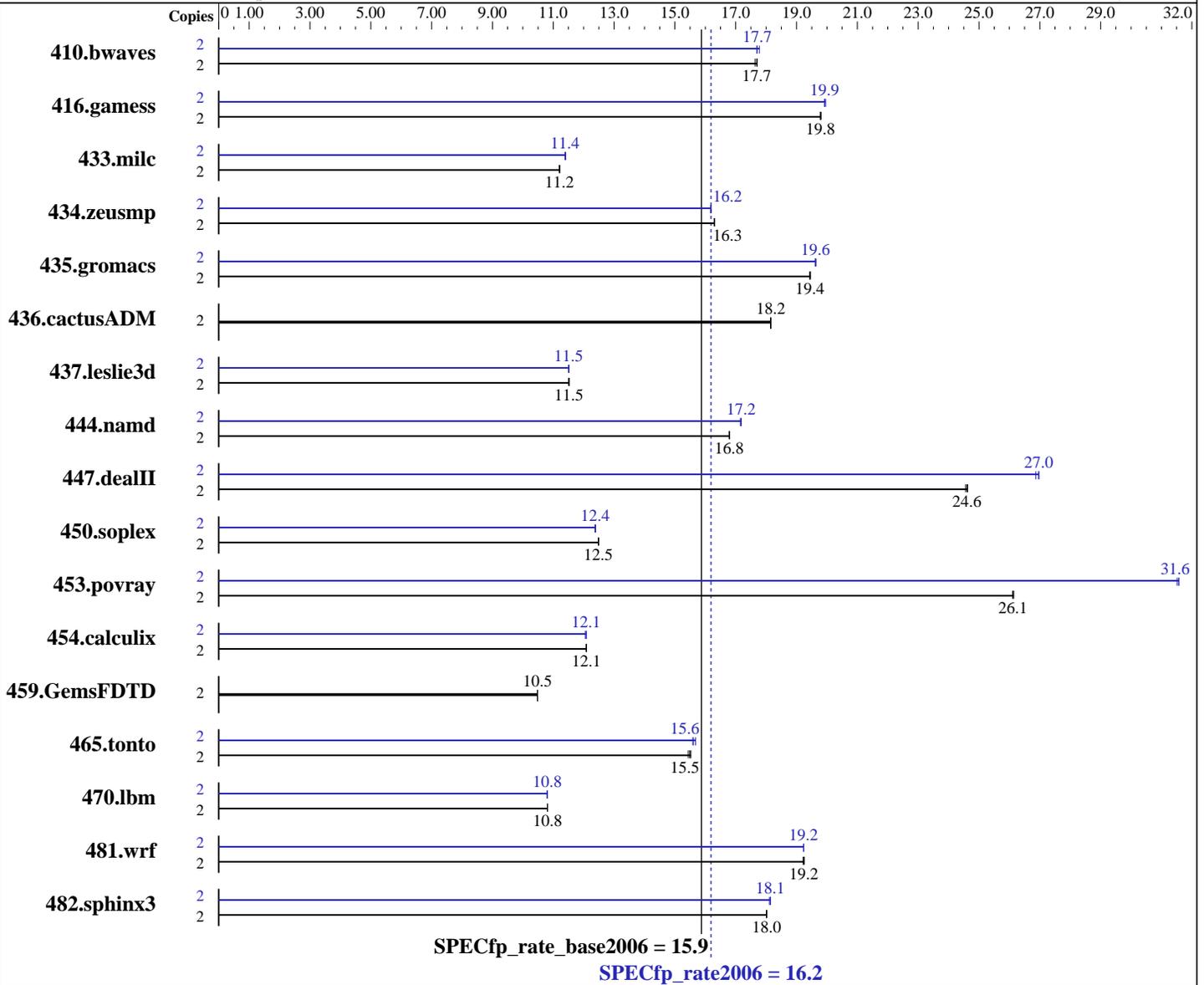
Test date: Jun-2007

Test sponsor: Intel Corporation

Hardware Availability: Jun-2007

Tested by: Intel Corporation

Software Availability: May-2007



### Hardware

CPU Name: Intel Core Duo T2600  
 CPU Characteristics: 2.17 GHz, 2MB L2, 667 MHz bus  
 CPU MHz: 2167  
 FPU: Integrated  
 CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip  
 CPU(s) orderable: 1 chip  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 2 MB I+D on chip per chip

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### Software

Operating System: Windows Vista32 Ultimate  
 Compiler: Intel C++ Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_CC\_P\_10.0.025  
 Intel Fortran Compiler for IA32 version 10.0  
 Build 20070426 Package ID: W\_FC\_P\_10.0.025  
 Microsoft Visual Studio .Net 2003 (for libraries)  
 Auto Parallel: No  
 File System: NTFS  
 System State: Default

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L3 Cache: None  
Other Cache: None  
Memory: 2 GB (2x1GB Hynix DDR2-667 CL5)  
Disk Subsystem: 100GB Hitachi SATA, 7200RPM  
Other Hardware: None

Base Pointers: 32-bit  
Peak Pointers: 32-bit  
Other Software: None  
SmartHeap Library Version 8.0 from  
<http://www.microquill.com/>

## Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1541	17.6	1535	17.7	<b>1536</b>	<b>17.7</b>	2	<b>1535</b>	<b>17.7</b>	1529	17.8	1536	17.7
416.gamess	2	<b>1979</b>	<b>19.8</b>	1980	19.8	1977	19.8	2	1962	20.0	1966	19.9	<b>1965</b>	<b>19.9</b>
433.milc	2	1639	11.2	1637	11.2	<b>1638</b>	<b>11.2</b>	2	1612	11.4	1609	11.4	<b>1612</b>	<b>11.4</b>
434.zeusmp	2	1116	16.3	<b>1116</b>	<b>16.3</b>	1117	16.3	2	1125	16.2	1124	16.2	<b>1125</b>	<b>16.2</b>
435.gromacs	2	734	19.5	735	19.4	<b>734</b>	<b>19.4</b>	2	728	19.6	<b>728</b>	<b>19.6</b>	727	19.6
436.cactusADM	2	<b>1317</b>	<b>18.2</b>	1317	18.1	1316	18.2	2	<b>1317</b>	<b>18.2</b>	1317	18.1	1316	18.2
437.leslie3d	2	<b>1633</b>	<b>11.5</b>	1631	11.5	1634	11.5	2	<b>1633</b>	<b>11.5</b>	1634	11.5	1633	11.5
444.namd	2	955	16.8	955	16.8	<b>955</b>	<b>16.8</b>	2	<b>934</b>	<b>17.2</b>	934	17.2	935	17.2
447.dealII	2	<b>930</b>	<b>24.6</b>	929	24.6	932	24.6	2	<b>849</b>	<b>27.0</b>	852	26.9	848	27.0
450.soplex	2	1337	12.5	1335	12.5	<b>1336</b>	<b>12.5</b>	2	1348	12.4	1346	12.4	<b>1347</b>	<b>12.4</b>
453.povray	2	407	26.1	408	26.1	<b>407</b>	<b>26.1</b>	2	<b>337</b>	<b>31.6</b>	338	31.5	337	31.6
454.calculix	2	<b>1365</b>	<b>12.1</b>	1365	12.1	1365	12.1	2	1369	12.1	1365	12.1	<b>1366</b>	<b>12.1</b>
459.GemsFDTD	2	2025	10.5	2024	10.5	<b>2024</b>	<b>10.5</b>	2	2025	10.5	2024	10.5	<b>2024</b>	<b>10.5</b>
465.tonto	2	<b>1270</b>	<b>15.5</b>	1274	15.4	1268	15.5	2	1255	15.7	1263	15.6	<b>1261</b>	<b>15.6</b>
470.lbm	2	2541	10.8	<b>2542</b>	<b>10.8</b>	2543	10.8	2	<b>2543</b>	<b>10.8</b>	2542	10.8	2545	10.8
481.wrf	2	1160	19.3	1162	19.2	<b>1161</b>	<b>19.2</b>	2	<b>1161</b>	<b>19.2</b>	1162	19.2	1161	19.2
482.sphinx3	2	2163	18.0	<b>2164</b>	<b>18.0</b>	2164	18.0	2	<b>2149</b>	<b>18.1</b>	2153	18.1	2148	18.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## General Notes

The system bus runs at 667 MHz  
System was configured with an nVIDIA GeForce Go 7900 GTX graphics card  
Binaries were built on Windows XP Professional SP2  
The start command with the /affinity switch was used to bind processes to cores

## Base Compiler Invocation

C benchmarks:  
icl -Qvc7.1 -Qc99  
  
C++ benchmarks:  
icl -Qvc7.1

Continued on next page



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**Software Availability:** May-2007

## Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icl -Qvc7.1 -Qc99 ifort

## Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore

444.namd: -TP

447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
-DBOOST\_NO\_INTRINSIC\_WCHAR\_T

453.povray: -DSPEC\_CPU\_WINDOWS\_ICL

454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase

481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Base Optimization Flags

C benchmarks:

-QxP -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

C++ benchmarks:

-QxP -Qipo -O3 -Qprec-div- -Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

Fortran benchmarks:

-QxP -Qipo -O3 -Qprec-div- /F950000000

Benchmarks using both Fortran and C:

-QxP -Qipo -O3 -Qprec-div- /F950000000

## Peak Compiler Invocation

C benchmarks:

icl -Qvc7.1 -Qc99

C++ benchmarks:

icl -Qvc7.1

Fortran benchmarks:

ifort

Continued on next page



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## Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:  
icl -Qvc7.1 -Qc99 ifort

## Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL\_II\_MEMBER\_VAR\_SPECIALIZATION\_BUG  
-DBOOST\_NO\_INTRINSIC\_WCHAR\_T  
453.povray: -DSPEC\_CPU\_WINDOWS\_ICL  
454.calculix: -DSPEC\_CPU\_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC\_CPU\_WINDOWS\_ICL

## Peak Optimization Flags

C benchmarks:

433.milc: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qunroll2 -Oa /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

470.lbm: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qunroll2 -Qscalar-rep- -Qprefetch /F950000000  
shlw32m.lib -link /FORCE:MULTIPLE

482.sphinx3: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qunroll2 /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Oa -Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

447.dealII: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qprefetch -Qcxx\_features /F950000000  
shlw32m.lib -link /FORCE:MULTIPLE

450.soplex: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qcxx\_features /F950000000 shlw32m.lib  
-link /FORCE:MULTIPLE

453.povray: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qansi-alias -Qcxx\_features /F950000000  
shlw32m.lib -link /FORCE:MULTIPLE

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## Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: -QxP -Qipo -O3 -Qprec-div- /F950000000  
416.gamess: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep-  
/F950000000  
434.zeusmp: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -O2 -Qprec\_div-  
-Qunroll10 -Qscalar-rep- /F950000000  
437.leslie3d: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- /F950000000  
459.GemsFDTD: basepeak = yes  
465.tonto: Same as 437.leslie3d

Benchmarks using both Fortran and C:

435.gromacs: -Qprof\_gen(pass 1) -Qprof\_use(pass 2) -QxP -Qipo -O3  
-Qprec-div- -Oa /F950000000  
436.cactusADM: basepeak = yes  
454.calculix: -QxP -Qipo -O3 -Qprec-div- /F950000000  
481.wrf: Same as 454.calculix

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.43.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.43.xml>

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