



SPEC® CINT2006 Result

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Acer Incorporated

SPECint®_rate2006 = 74.1

Acer Altos R720 (Intel Xeon E5320)

SPECint_rate_base2006 = 68.4

CPU2006 license: 97

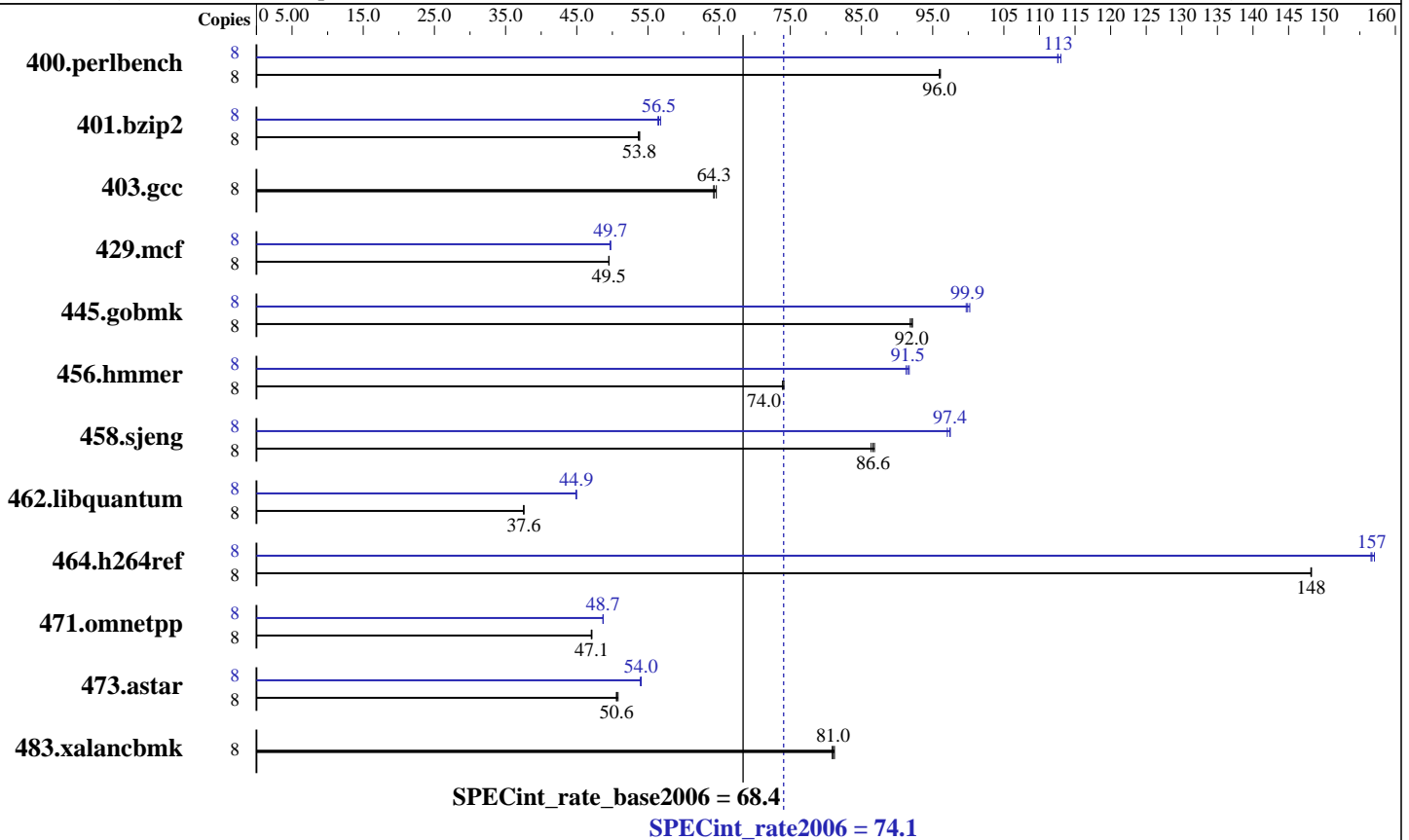
Test sponsor: Acer Incorporated

Tested by: Acer Incorporated

Test date: Aug-2007

Hardware Availability: Nov-2006

Software Availability: Jun-2007



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1066MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1, 2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
 L3 Cache: None
 Other Cache: None
 Memory: 16 GB (8 x 2048MB ECC FB-DIMM DDR2-667 CL5-5-5)
 Disk Subsystem: 1 x 73GB 10000RPM SAS HDD
 Other Hardware: None

Software

Operating System: SuSE Linux Enterprise Server 10 EM64T (kernel 2.6.16.21-0.8-smp)
 Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.0
 Build 20070426 Package ID: l_cc_p_10.0.023
 Auto Parallel: No
 File System: ReiserFS
 System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32/64-bit
 Other Software: MicroQuill SmartHeap Library 8.1



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Results Table

| Benchmark | Base | | | | | | Peak | | | | | | | |
|----------------|--------|-------------|-------------|------------|-------------|-------------|-------------|--------|-------------|-------------|-------------|-------------|-------------|-------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 400.perlbench | 8 | 813 | 96.1 | 814 | 96.0 | 815 | 95.9 | 8 | 692 | 113 | 694 | 113 | 694 | 113 |
| 401.bzip2 | 8 | 1439 | 53.7 | 1434 | 53.8 | 1435 | 53.8 | 8 | 1360 | 56.8 | 1366 | 56.5 | 1369 | 56.4 |
| 403.gcc | 8 | 1003 | 64.2 | 997 | 64.6 | 1002 | 64.3 | 8 | 1003 | 64.2 | 997 | 64.6 | 1002 | 64.3 |
| 429.mcf | 8 | 1474 | 49.5 | 1473 | 49.5 | 1474 | 49.5 | 8 | 1467 | 49.7 | 1466 | 49.8 | 1468 | 49.7 |
| 445.gobmk | 8 | 911 | 92.2 | 912 | 92.0 | 914 | 91.8 | 8 | 840 | 99.9 | 837 | 100 | 842 | 99.7 |
| 456.hammer | 8 | 1009 | 74.0 | 1010 | 73.9 | 1007 | 74.1 | 8 | 816 | 91.5 | 814 | 91.7 | 818 | 91.3 |
| 458.sjeng | 8 | 1115 | 86.8 | 1121 | 86.4 | 1118 | 86.6 | 8 | 997 | 97.1 | 993 | 97.5 | 994 | 97.4 |
| 462.libquantum | 8 | 4414 | 37.5 | 4413 | 37.6 | 4414 | 37.6 | 8 | 3689 | 44.9 | 3692 | 44.9 | 3686 | 45.0 |
| 464.h264ref | 8 | 1195 | 148 | 1195 | 148 | 1194 | 148 | 8 | 1130 | 157 | 1131 | 157 | 1127 | 157 |
| 471.omnetpp | 8 | 1061 | 47.1 | 1063 | 47.0 | 1062 | 47.1 | 8 | 1027 | 48.7 | 1027 | 48.7 | 1027 | 48.7 |
| 473.astar | 8 | 1105 | 50.8 | 1111 | 50.6 | 1109 | 50.6 | 8 | 1041 | 53.9 | 1039 | 54.0 | 1039 | 54.1 |
| 483.xalanbmk | 8 | 680 | 81.2 | 682 | 80.9 | 681 | 81.0 | 8 | 680 | 81.2 | 682 | 80.9 | 681 | 81.0 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Acer Altos R520
The Altos R720 and Altos R520 are electronically equivalent.

BIOS settings
Hardware Prefetch is set to disabled
Adjacent Cache Line Prefetch is set to disabled
taskset was used to bind processes to cores

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalanbmk: -DSPEC_CPU_LINUX



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Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

Continued on next page



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Peak Optimization Flags (Continued)

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias

456.hmmer: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmer

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec_div -ansi-alias -Wl,-z,muldefs
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.16.xml>



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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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