



SPEC[®] CFP2006 Result

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NEC Corporation

Express5800/i120Rg-1
(Intel Xeon processor LV 5148)

SPECfp[®]2006 = 14.9

SPECfp_base2006 = 14.4

CPU2006 license: 9006

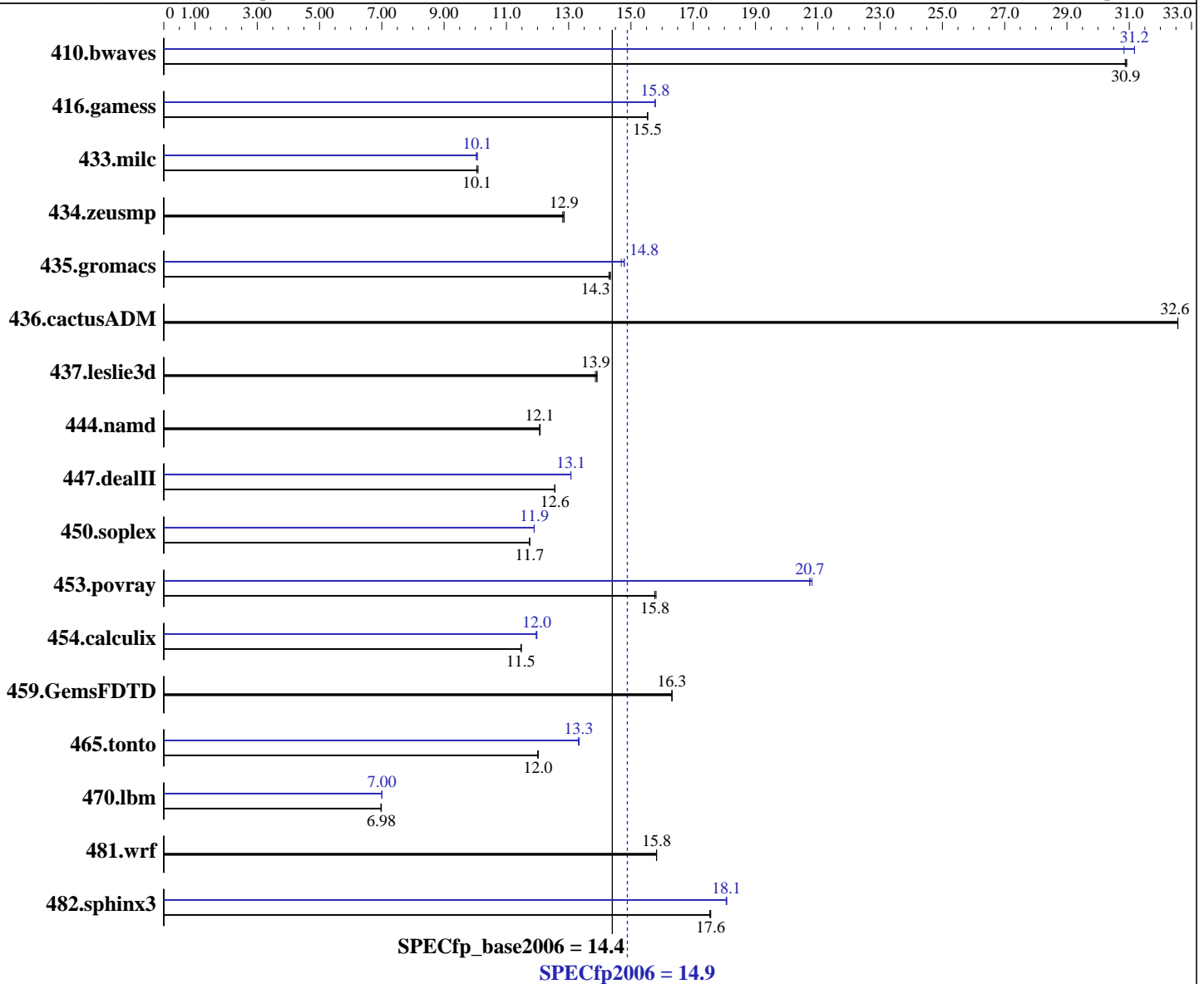
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2007

Hardware Availability: May-2007

Software Availability: Apr-2007



Hardware

CPU Name: Intel Xeon 5148 LV
 CPU Characteristics: 2.33 GHz, 4MB L2, 1333MHz bus
 CPU MHz: 2333
 FPU: Integrated
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 4 MB I+D on chip per chip

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Software

Operating System: Windows Server 2003, Standard x64 Edition
 Compiler: Intel C++ Compiler for EM64T version 9.1
 Build 20070322, Package-ID W_CC_C_9.1.037
 Intel Fortran Compiler for EM64T version 9.1
 Build 20070322, Package-ID W_FC_C_9.1.037
 Microsoft Visual Studio 2005 (libr. & linker)
 Auto Parallel: Yes
 File System: NTFS
 System State: Default

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000RPM
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: None

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	440	30.9	440	30.9	440	30.9	<u>436</u>	<u>31.2</u>	436	31.2	441	30.8
416.gamess	1261	15.5	1261	15.5	1260	15.5	1241	15.8	<u>1241</u>	<u>15.8</u>	1241	15.8
433.milc	910	10.1	913	10.1	913	10.1	912	10.1	<u>912</u>	<u>10.1</u>	915	10.0
434.zeusmp	710	12.8	708	12.9	708	12.9	710	12.8	<u>708</u>	<u>12.9</u>	708	12.9
435.gromacs	500	14.3	498	14.3	498	14.3	483	14.8	486	14.7	483	14.8
436.cactusADM	367	32.6	367	32.6	367	32.6	367	32.6	367	32.6	367	32.6
437.leslie3d	676	13.9	678	13.9	676	13.9	676	13.9	678	13.9	676	13.9
444.namd	664	12.1	665	12.1	665	12.1	664	12.1	665	12.1	665	12.1
447.dealII	911	12.6	911	12.6	911	12.6	875	13.1	875	13.1	875	13.1
450.soplex	710	11.7	710	11.7	710	11.7	701	11.9	<u>701</u>	<u>11.9</u>	701	11.9
453.povray	337	15.8	337	15.8	337	15.8	256	20.7	<u>256</u>	<u>20.7</u>	256	20.8
454.calculix	720	11.5	718	11.5	720	11.5	690	12.0	689	12.0	689	12.0
459.GemsFDTD	650	16.3	650	16.3	650	16.3	650	16.3	650	16.3	650	16.3
465.tonto	819	12.0	819	12.0	818	12.0	738	13.3	<u>739</u>	<u>13.3</u>	739	13.3
470.lbm	1967	6.98	1971	6.97	1967	6.98	1962	7.00	1962	7.00	1962	7.00
481.wrf	706	15.8	706	15.8	706	15.8	706	15.8	706	15.8	706	15.8
482.sphinx3	1112	17.5	1110	17.6	1110	17.6	1079	18.1	1079	18.1	1078	18.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
icl -Qvc8 -Qc99

C++ benchmarks:
icl -Qvc8

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort



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Base Portability Flags

```

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -D_Complex= -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -D_Complex= -DSPEC_CPU_P64
436.cactusADM: -D_Complex= -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -D_Complex= -DSPEC_CPU_P64 -DBOOST_NO_INTRINSIC_WCHAR_T
-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER
-Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -D_Complex= -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -D_Complex= -DSPEC_CPU_P64

```

Base Optimization Flags

```

C benchmarks:
  -fast -Qparallel -F950000000 -link -FORCE:MULTIPLE

C++ benchmarks:
  -fast -Qparallel -Qcxx-features -F950000000
  -link -FORCE:MULTIPLE

Fortran benchmarks:
  -fast -Qparallel -F950000000 -link -FORCE:MULTIPLE

Benchmarks using both Fortran and C:
  -fast -Qparallel -F950000000 -link -FORCE:MULTIPLE

```

Peak Compiler Invocation

```

C benchmarks:
  icl -Qvc8 -Qc99

C++ benchmarks:
  icl -Qvc8

Fortran benchmarks:
  ifort

```

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Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc8 -Qc99 ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

-Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000
-link -FORCE:MULTIPLE

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features
-F950000000 -link -FORCE:MULTIPLE

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel
-F950000000 -link -FORCE:MULTIPLE

416.gamess: -fast -F950000000 -link -FORCE:MULTIPLE

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: Same as 410.bwaves

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F950000000
-link -FORCE:MULTIPLE

436.cactusADM: basepeak = yes

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Peak Optimization Flags (Continued)

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-cpu2006-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-cpu2006-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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