



SPEC® CFP2006 Result

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Hewlett-Packard Company

SPECfp®_rate2006 = 25.8

ProLiant ML350 G5
(1.86 GHz, Intel Xeon processor E5320)

SPECfp_rate_base2006 = 25.4

CPU2006 license: 3

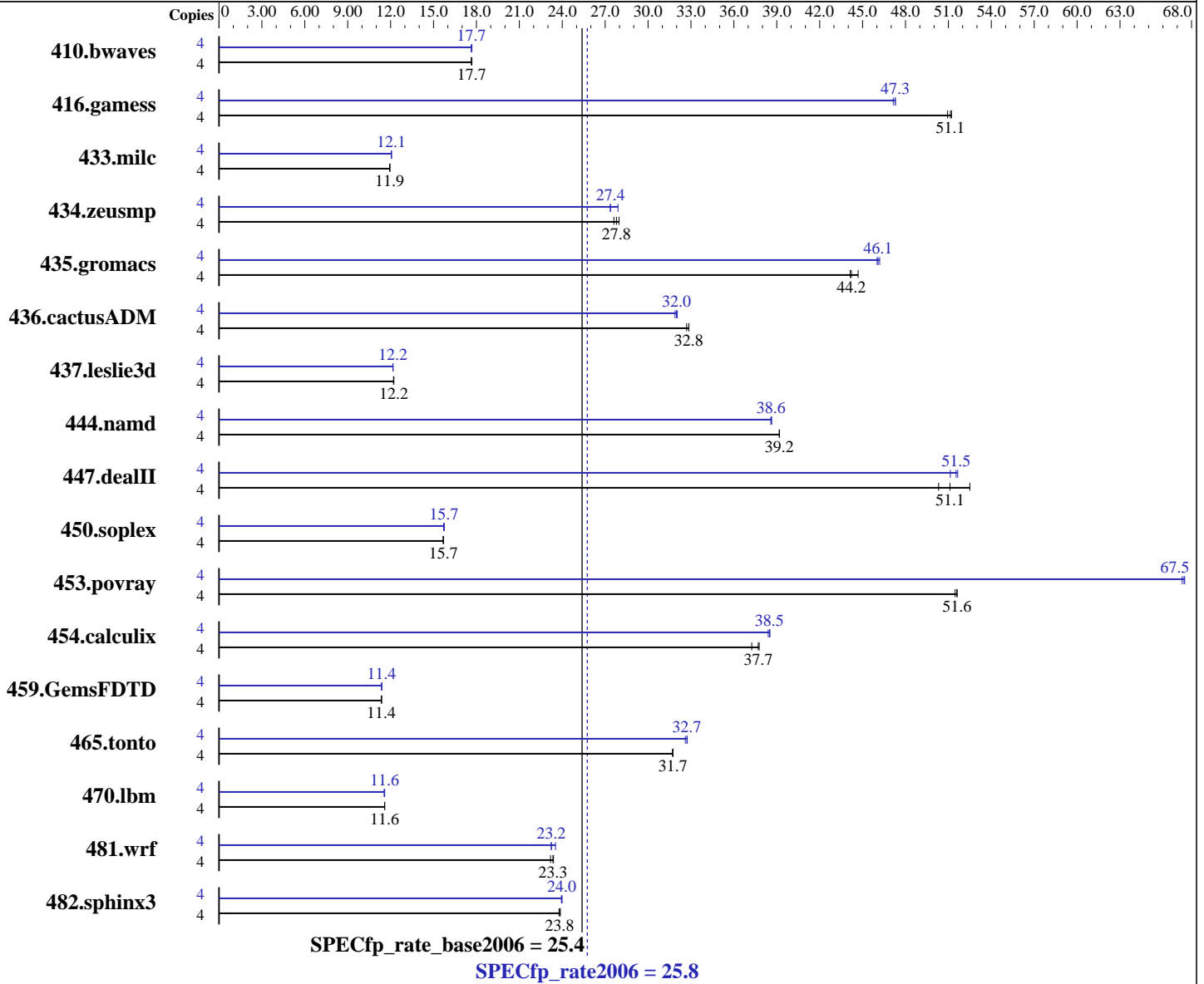
Test date: Feb-2007

Test sponsor: Hewlett-Packard Company

Hardware Availability: Nov-2006

Tested by: Hewlett-Packard Company

Software Availability: Nov-2006



Hardware

CPU Name: Intel Xeon E5320
 CPU Characteristics: 1.86 GHz, 2x4 MB L2 shared, 1066 MHz system bus
 CPU MHz: 1860
 FPU: Integrated
 CPU(s) enabled: 4 cores, 1 chip, 4 cores/chip
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
 Compiler: Intel C++ Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_cc_c_9.1.045
 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1
 Build 20061101, Package ID: 1_fc_c_9.1.040
 Auto Parallel: No

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-5300F CL5)
Disk Subsystem: 4x36 GB 10 K SAS
Other Hardware: None

File System: ext2
System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	3076	17.7	3074	17.7	3082	17.6	4	3079	17.7	3073	17.7	3081	17.6
416.gamess	4	1529	51.2	1537	50.9	1531	51.1	4	1656	47.3	1661	47.1	1656	47.3
433.milc	4	3077	11.9	3073	11.9	3075	11.9	4	3045	12.1	3046	12.1	3046	12.1
434.zeusmp	4	1310	27.8	1301	28.0	1318	27.6	4	1305	27.9	1330	27.4	1331	27.4
435.gromacs	4	647	44.1	639	44.7	646	44.2	4	621	46.0	620	46.1	618	46.2
436.cactusADM	4	1462	32.7	1455	32.8	1455	32.9	4	1492	32.0	1499	31.9	1494	32.0
437.leslie3d	4	3078	12.2	3075	12.2	3081	12.2	4	3091	12.2	3090	12.2	3091	12.2
444.namd	4	819	39.2	819	39.2	819	39.2	4	830	38.6	831	38.6	831	38.6
447.dealII	4	895	51.1	872	52.5	909	50.3	4	895	51.1	886	51.7	888	51.5
450.soplex	4	2125	15.7	2127	15.7	2129	15.7	4	2119	15.7	2124	15.7	2119	15.7
453.povray	4	413	51.5	412	51.6	412	51.6	4	316	67.3	315	67.5	315	67.5
454.calculix	4	874	37.8	875	37.7	886	37.3	4	857	38.5	857	38.5	859	38.4
459.GemsFDTD	4	3735	11.4	3733	11.4	3739	11.4	4	3731	11.4	3730	11.4	3731	11.4
465.tonto	4	1240	31.7	1241	31.7	1240	31.7	4	1207	32.6	1202	32.7	1203	32.7
470.lbm	4	4745	11.6	4746	11.6	4745	11.6	4	4760	11.5	4745	11.6	4745	11.6
481.wrf	4	1910	23.4	1914	23.3	1928	23.2	4	1922	23.2	1924	23.2	1899	23.5
482.sphinx3	4	3279	23.8	3267	23.9	3273	23.8	4	3251	24.0	3256	23.9	3251	24.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
"ulimit -s unlimited" set

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Base Compiler Invocation (Continued)

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 450.soplex: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
 482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks:

icc

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Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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