SPEC® CINT2006 Result
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Bull SAS

NovaScale T840 (3.00 GHz, Intel Xeon 5160)

SPECint®_rate2006 = 50.7
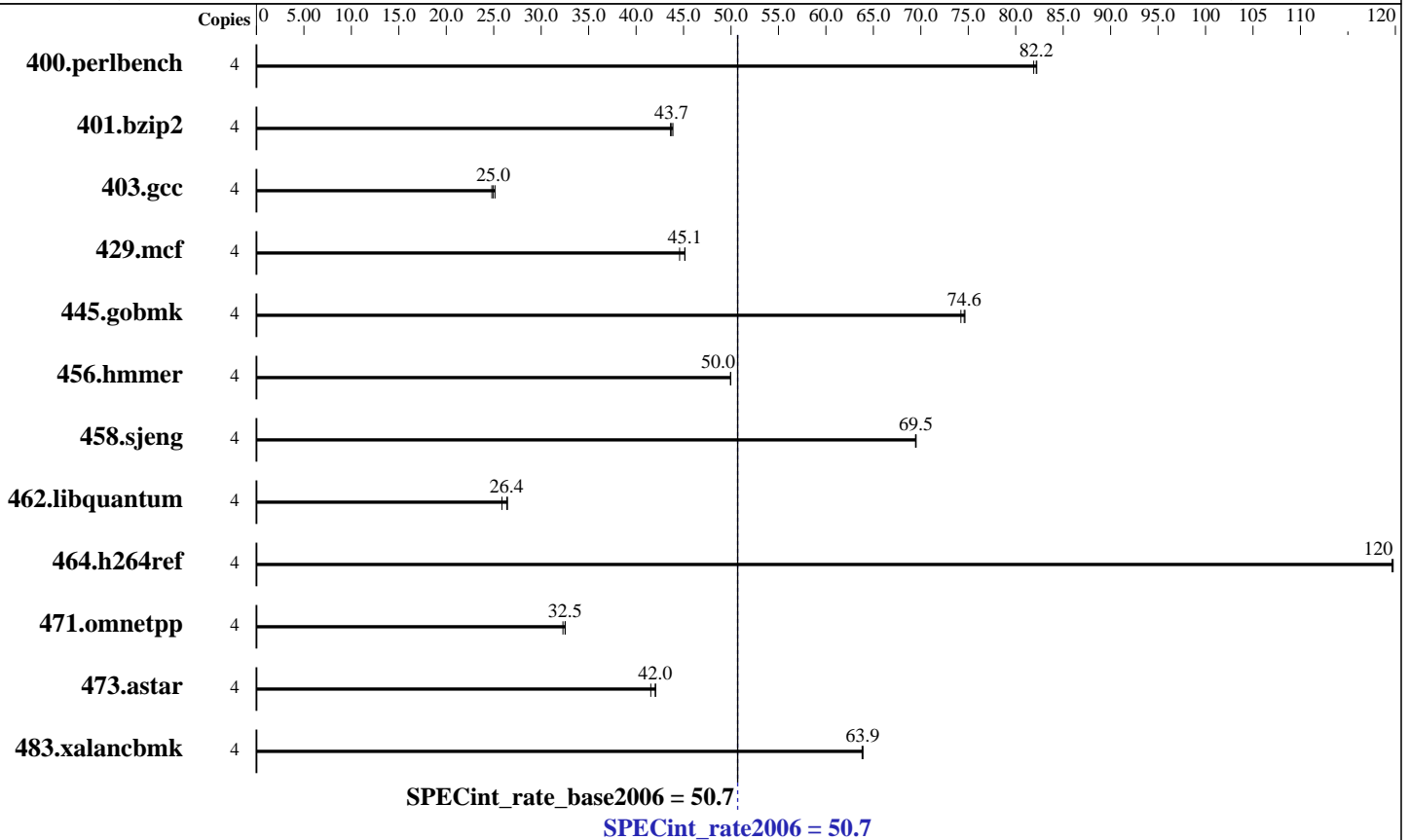
SPECint_rate_base2006 = 50.7

CPU2006 license: 3
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Feb-2007
Hardware Availability: Jul-2006
Software Availability: Dec-2006

Copies

Benchmark	Copies	Result
400.perlbench	4	82.2
401.bzip2	4	43.7
403.gcc	4	25.0
429.mcf	4	45.1
445.gobmk	4	74.6
456.hmmer	4	50.0
458.sjeng	4	69.5
462.libquantum	4	26.4
464.h264ref	4	120
471.omnetpp	4	32.5
473.astar	4	42.0
483.xalancbmk	4	63.9

SPECint_rate_base2006 = 50.7
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Hardware

CPU Name: Intel Xeon 5160
CPU Characteristics: 3.0GHz, 1332MHz bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 8 GB (667 MHz ECC CL5 DDR2 FB-DIMM)
Disk Subsystem: 3x73GB SCSI 15000 rpm
Other Hardware: None

Software

Operating System: Windows Server 2003 Enterprise X64 Edition
Compiler: Intel C++ Compiler 9.1.033 for 32-bit apps,
Build 20061103Z Package ID: W_CC_P_9.1.033
Microsoft Visual Studio .NET 2003 (libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 64-bit
Peak Pointers: Not Applicable
Other Software: MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Results Table

Benchmark	Base						Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	477	81.9	475	82.2	**476**	**82.2**	4	477	81.9	475	82.2	**476**	**82.2**
401.bzip2	4	885	43.6	880	43.9	**882**	**43.7**	4	885	43.6	880	43.9	**882**	**43.7**
403.gcc	4	1298	24.8	1281	25.1	**1290**	**25.0**	4	1298	24.8	1281	25.1	**1290**	**25.0**
429.mcf	4	818	44.6	**808**	**45.1**	808	45.1	4	818	44.6	**808**	**45.1**	808	45.1
445.gobmk	4	**563**	**74.6**	562	74.7	565	74.2	4	**563**	**74.6**	562	74.7	565	74.2
456.hmmer	4	747	50.0	747	50.0	**747**	**50.0**	4	747	50.0	747	50.0	**747**	**50.0**
458.sjeng	4	697	69.4	696	69.5	**697**	**69.5**	4	697	69.4	696	69.5	**697**	**69.5**
462.libquantum	4	3205	25.9	3133	26.5	**3143**	**26.4**	4	3205	25.9	3133	26.5	**3143**	**26.4**
464.h264ref	4	**740**	**120**	740	120	739	120	4	**740**	**120**	740	120	739	120
471.omnetpp	4	774	32.3	768	32.5	**769**	**32.5**	4	774	32.3	768	32.5	**769**	**32.5**
473.astar	4	676	41.5	**668**	**42.0**	668	42.1	4	676	41.5	**668**	**42.0**	668	42.1
483.xalancbmk	4	**432**	**63.9**	432	63.9	433	63.8	4	**432**	**63.9**	432	63.9	433	63.8

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
         -link /FORCE:MULTIPLE
```

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Base Other Flags

C benchmarks:

    403.gcc: `-Dalloca=_alloca`

Peak Optimization Flags

C benchmarks:

    400.perlbench: `basepeak = yes`

    401.bzip2: `basepeak = yes`

    403.gcc: `basepeak = yes`

    429.mcf: `basepeak = yes`

    445.gobmk: `basepeak = yes`

    456.hmmer: `basepeak = yes`

    458.sjeng: `basepeak = yes`

    462.libquantum: `basepeak = yes`

    464.h264ref: `basepeak = yes`

C++ benchmarks:

    471.omnetpp: `basepeak = yes`

    473.astar: `basepeak = yes`

    483.xalancbmk: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/flags.20090714.00.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/flags.20090714.00.xml`

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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