



SPEC[®] CFP2006 Result

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Itautec

SPECfp[®]_rate2006 = 48.4

Servidor Itautec MX201 (Intel Xeon E5335)

SPECfp_rate_base2006 = 48.4

CPU2006 license: 9001

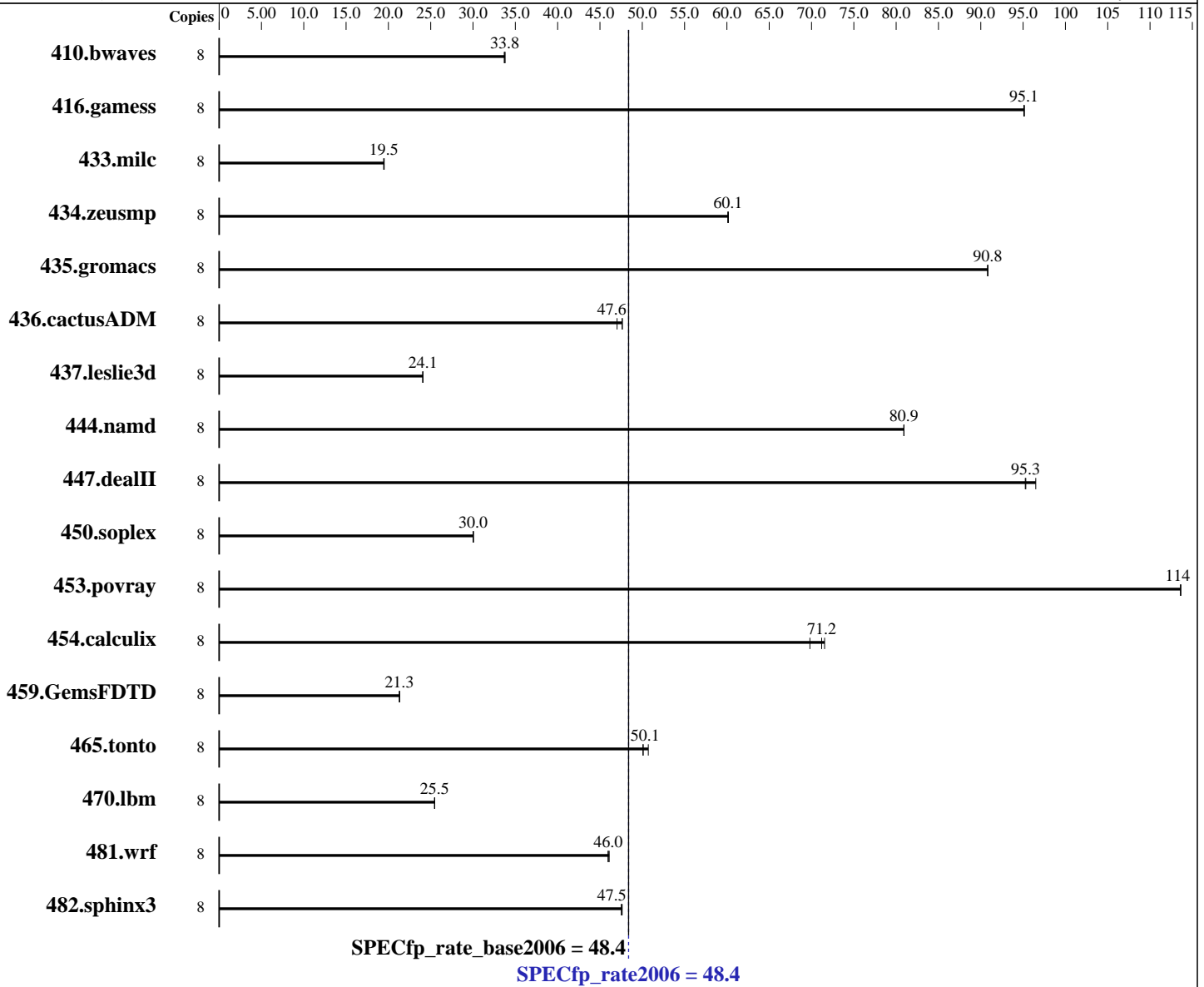
Test date: Feb-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006



Hardware

CPU Name: Intel Xeon E5335
 CPU Characteristics: 1333MHz system bus
 CPU MHz: 2000
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
 CPU(s) orderable: 1-2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
 Compiler: Intel C++ Compiler for IA32 version 9.1
 Package ID W_CC_C_9.1.025 Build no 20060519Z
 Intel Fortran Compiler for IA32 version 9.1
 Package ID W_FC_C_9.1.025 Build no 20060519Z
 Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
 Auto Parallel: No
 File System: NTFS

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 73 GB SCSI, 10000RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3228	33.7	<u>3220</u>	<u>33.8</u>	3218	33.8	8	3228	33.7	<u>3220</u>	<u>33.8</u>	3218	33.8
416.gamess	8	<u>1647</u>	<u>95.1</u>	1647	95.1	1646	95.2	8	<u>1647</u>	<u>95.1</u>	1647	95.1	1646	95.2
433.milc	8	<u>3771</u>	<u>19.5</u>	3777	19.4	3765	19.5	8	<u>3771</u>	<u>19.5</u>	3777	19.4	3765	19.5
434.zeusmp	8	<u>1211</u>	<u>60.1</u>	1210	60.2	1211	60.1	8	<u>1211</u>	<u>60.1</u>	1210	60.2	1211	60.1
435.gromacs	8	629	90.8	<u>629</u>	<u>90.8</u>	629	90.8	8	629	90.8	<u>629</u>	<u>90.8</u>	629	90.8
436.cactusADM	8	2033	47.0	2007	47.6	<u>2007</u>	<u>47.6</u>	8	2033	47.0	2007	47.6	<u>2007</u>	<u>47.6</u>
437.leslie3d	8	3123	24.1	<u>3124</u>	<u>24.1</u>	3125	24.1	8	3123	24.1	<u>3124</u>	<u>24.1</u>	3125	24.1
444.namd	8	<u>793</u>	<u>80.9</u>	793	80.9	793	80.9	8	<u>793</u>	<u>80.9</u>	793	80.9	793	80.9
447.dealII	8	<u>960</u>	<u>95.3</u>	948	96.5	961	95.3	8	<u>960</u>	<u>95.3</u>	948	96.5	961	95.3
450.soplex	8	<u>2222</u>	<u>30.0</u>	2222	30.0	2221	30.0	8	<u>2222</u>	<u>30.0</u>	2222	30.0	2221	30.0
453.povray	8	375	114	<u>375</u>	<u>114</u>	375	114	8	375	114	<u>375</u>	<u>114</u>	375	114
454.calculix	8	922	71.5	<u>927</u>	<u>71.2</u>	945	69.8	8	922	71.5	<u>927</u>	<u>71.2</u>	945	69.8
459.GemsFDTD	8	3978	21.3	3986	21.3	<u>3983</u>	<u>21.3</u>	8	3978	21.3	3986	21.3	<u>3983</u>	<u>21.3</u>
465.tonto	8	<u>1571</u>	<u>50.1</u>	1552	50.7	1572	50.1	8	<u>1571</u>	<u>50.1</u>	1552	50.7	1572	50.1
470.lbm	8	4318	25.5	<u>4317</u>	<u>25.5</u>	4316	25.5	8	4318	25.5	<u>4317</u>	<u>25.5</u>	4316	25.5
481.wrf	8	1945	46.0	1939	46.1	<u>1943</u>	<u>46.0</u>	8	1945	46.0	1939	46.1	<u>1943</u>	<u>46.0</u>
482.sphinx3	8	<u>3280</u>	<u>47.5</u>	3280	47.5	3274	47.6	8	<u>3280</u>	<u>47.5</u>	3280	47.5	3274	47.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

This result was measured on the Servidor Itaotec MX221.
The Servidor Itaotec MX221 and the Servidor Itaotec MX201 are electronically equivalent.

Base Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlSMPMt.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:
433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: basepeak = yes

450.soplex: basepeak = yes

453.povray: basepeak = yes

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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