SPEC® CINT2006 Result
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IBM Corporation

IBM BladeCenter HS21 XM (Intel Xeon 5130)

SPECint®_rate2006 = Not Run

SPECint_rate_base2006 = 41.0

CPU2006 license: 11
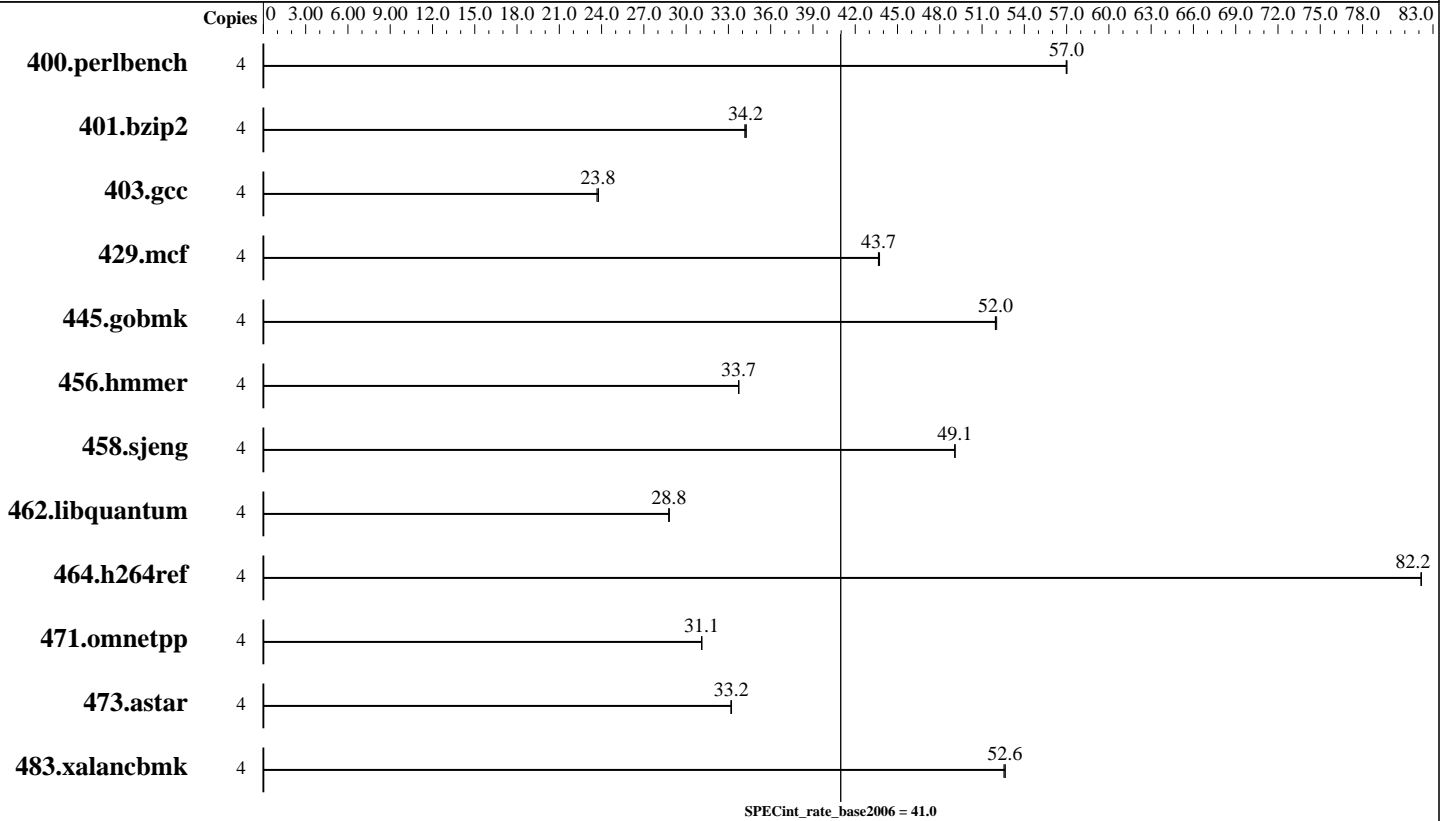**Test sponsor:** IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2007
Hardware Availability: Feb-2007
Software Availability: Aug-2006

Copies		Result
400.perlbench	4	57.0
401.bzip2	4	34.2
403.gcc	4	23.8
429.mcf	4	43.7
445.gobmk	4	52.0
456.hmmer	4	33.7
458.sjeng	4	49.1
462.libquantum	4	28.8
464.h264ref	4	82.2
471.omnetpp	4	31.1
473.astar	4	33.2
483.xalancbmk	4	52.6

Scale: 0 3.00 6.00 9.00 12.0 15.0 18.0 21.0 24.0 27.0 30.0 33.0 36.0 39.0 42.0 45.0 48.0 51.0 54.0 57.0 60.0 63.0 66.0 69.0 72.0 75.0 78.0 83.0

SPECint_rate_base2006 = 41.0

Hardware

CPU Name:	Intel Xeon 5130
CPU Characteristics:	1333MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem:	1 x 74 GB SAS, 1000 RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	686	57.0	**685**	**57.0**	685	57.0							
401.bzip2	4	1130	34.2	**1128**	**34.2**	1126	34.3							
403.gcc	4	1354	23.8	1360	23.7	**1355**	**23.8**							
429.mcf	4	834	43.7	836	43.6	**835**	**43.7**							
445.gobmk	4	808	51.9	806	52.0	**808**	**52.0**							
456.hmmer	4	1106	33.7	1106	33.7	**1106**	**33.7**							
458.sjeng	4	**986**	**49.1**	986	49.1	986	49.1							
462.libquantum	4	2881	28.8	**2880**	**28.8**	2877	28.8							
464.h264ref	4	1077	82.2	1077	82.2	**1077**	**82.2**							
471.omnetpp	4	804	31.1	**804**	**31.1**	804	31.1							
473.astar	4	845	33.2	**846**	**33.2**	846	33.2							
483.xalancbmk	4	524	52.6	525	52.6	**524**	**52.6**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Base Portability Flags

```
    403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
-fast /F512000000 shlw32m.lib          -link /FORCE:MULTIPLE
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib
         -link /FORCE:MULTIPLE
```

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Base Other Flags

C benchmarks:

> 403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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