SPEC® CINT2006 Result
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Intel Corporation

Intel DG965SS motherboard
(1.8 GHz, Intel Core 2 Duo processor E4300)

$SPECint^{®}\_rate2006 =$ Not Run

$SPECint\_rate\_base2006 =$ 19.8
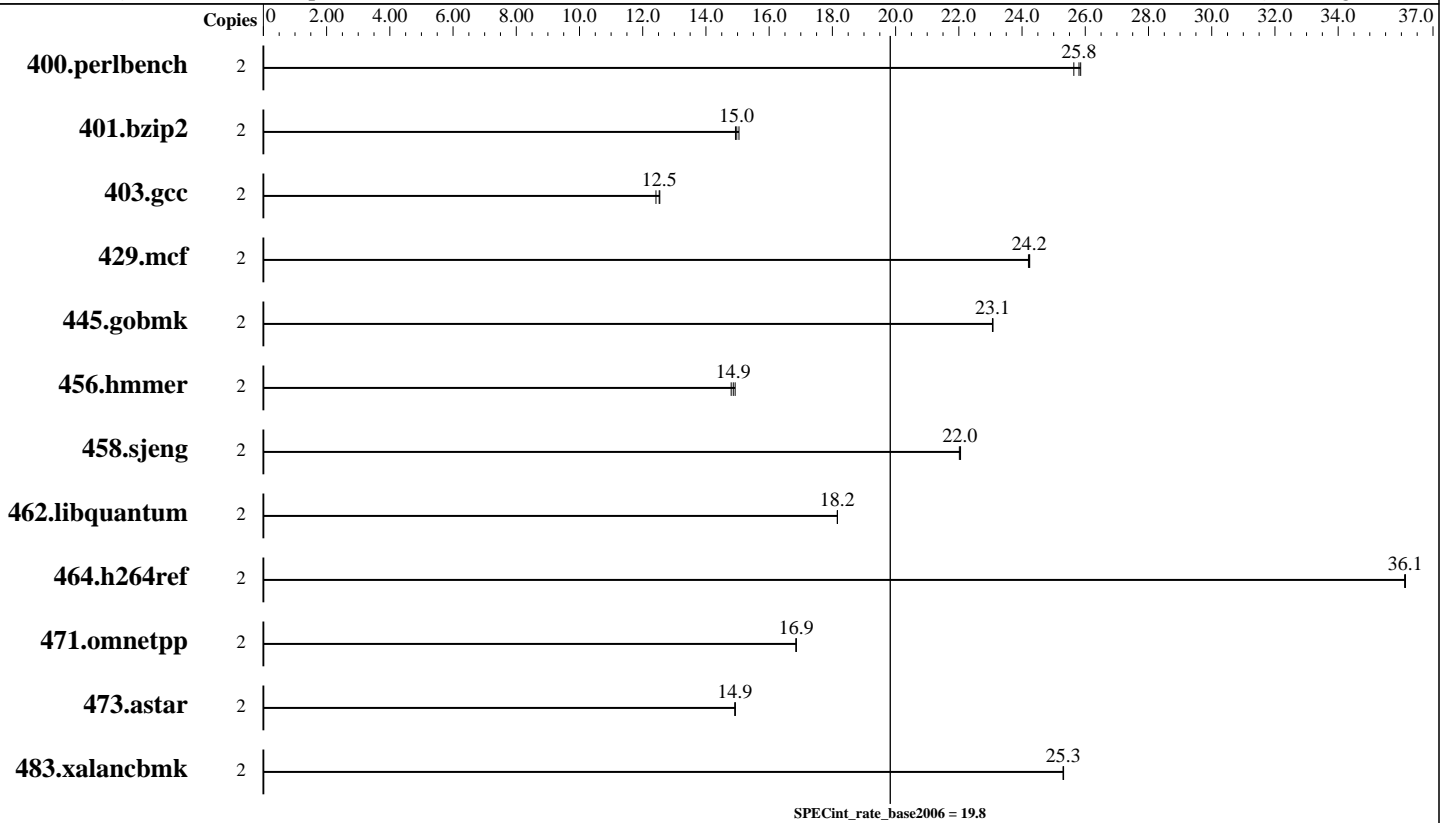
CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Aug-2006

Copies		
400.perlbench	2	25.8
401.bzip2	2	15.0
403.gcc	2	12.5
429.mcf	2	24.2
445.gobmk	2	23.1
456.hmmer	2	14.9
458.sjeng	2	22.0
462.libquantum	2	18.2
464.h264ref	2	36.1
471.omnetpp	2	16.9
473.astar	2	14.9
483.xalancbmk	2	25.3

SPECint_rate_base2006 = 19.8

Hardware
CPU Name: Intel Core 2 Duo E4300
CPU Characteristics: 1.8 GHz, 800 MHz bus
CPU MHz: 1800
FPU: Integrated
CPU(s) enabled: 2 cores, 1 chip, 2 cores/chip
CPU(s) orderable: 1 chip
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 2 MB I+D on chip per chip
L3 Cache: None
Other Cache: None
Memory: 2 GB(2 1GB Micron MT16HTF12864AY-80ED4 DDR2 800, CL5)
Disk Subsystem: Maxtor DiamondMax 10 6B300S0 300GB NCQ Serial ATA (7200 RPM, 16MB cache)
Other Hardware: SoundBlaster Live! PCI card

Software
Operating System: Windows XP Professional w/ SP2
Compiler: Intel C++ Compiler for IA32 version 9.1 Build no 20060816
Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel: No
File System: NTFS
System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

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Results Table

Benchmark	Base Copies	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Base Seconds	Base Ratio	Peak Copies	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio	Peak Seconds	Peak Ratio
400.perlbench	2	756	25.9	762	25.6	**757**	**25.8**							
401.bzip2	2	1292	14.9	**1289**	**15.0**	1283	15.0							
403.gcc	2	1284	12.5	**1286**	**12.5**	1296	12.4							
429.mcf	2	754	24.2	752	24.2	**753**	**24.2**							
445.gobmk	2	910	23.1	909	23.1	**909**	**23.1**							
456.hmmer	2	1261	14.8	**1255**	**14.9**	1250	14.9							
458.sjeng	2	1099	22.0	**1098**	**22.0**	1097	22.1							
462.libquantum	2	**2282**	**18.2**	2283	18.2	2282	18.2							
464.h264ref	2	1226	36.1	1225	36.1	**1225**	**36.1**							
471.omnetpp	2	742	16.8	742	16.9	**742**	**16.9**							
473.astar	2	**941**	**14.9**	941	14.9	940	14.9							
483.xalancbmk	2	545	25.3	**545**	**25.3**	545	25.3							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
   Tested systems can be used with Shin-G ATX case, Antec NeoPower 480W power supply
   Product description located as of 1/2007:
   http://www.intel.com/products/motherboard/DG965SS/index.htm
   The system bus runs at 800 MHz
```

Base Compiler Invocation

C benchmarks:
```
     icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
     icl -Qvc7.1
```

Base Portability Flags

```
       403.gcc: -DSPEC_CPU_WIN32
    464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32
```

Base Optimization Flags

C benchmarks:
```
     -fast /F512000000 shlw32m.lib              -link /FORCE:MULTIPLE
```

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Test sponsor:	Intel Corporation	**Hardware Availability:**	Jan-2007
Tested by:	Intel Corporation	**Software Availability:**	Aug-2006

Base Optimization Flags (Continued)

C++ benchmarks:
```
    -fast -Qcxx_features /F512000000 shlw32m.lib
                -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

       403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.04.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.04.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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