



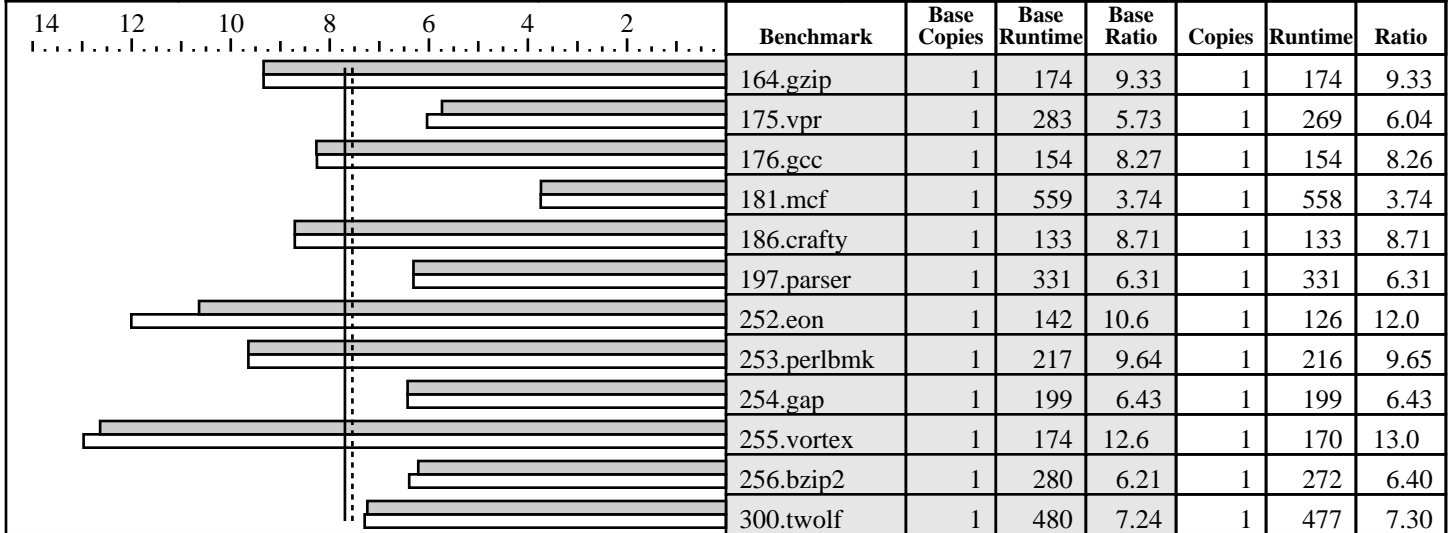
CINT2000 Result

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Dell
PowerEdge 1500SC (1.4 GHz PIII)

SPECint_rate2000 = **7.70**
SPECint_rate_base2000 = **7.55**

SPEC license #: 55 | Tested by: Dell, Round Rock, TX | Test date: Dec-2001 | Hardware Avail: Jan-2002 | Software Avail: Jun-2001



Hardware

CPU: Intel Pentium III
 CPU MHz: 1400
 FPU: Integrated
 CPU(s) enabled: 1 core, 1 chip, 1 core/chip
 CPU(s) orderable: 1, 2
 Parallel: No
 Primary Cache: 16KB(I) + 16KB(D) on chip
 Secondary Cache: 512KB(I+D) on chip
 L3 Cache: N/A
 Other Cache: N/A
 Memory: 1024MB SDRAM
 Disk Subsystem: 1 x 9GB Quantum Atlas 10K II U160
 Other Hardware:

Software

Operating System: Windows 2000 Server (SP2 Build 2195)
 Compiler: Intel C++ Compiler 5.0.1 (010727Z)
 Microsoft Visual C++ 6.0 (SP5)
 MicroQuill SmartHeap Library 5.0
 File System: NTFS
 System State: Default

Notes/Tuning Information

PORTABILITY FLAGS

176.gcc: -Dalloca=_alloca /F10000000
 186.crafy: -DNT_i386
 253.perlbnk: -DSPEC_CPU2000_NTOS -DPERLDLL /MT
 254.gap: -DSYS_HAS_CALLOC_PROTO -DSYS_HAS_MALLOC_PROTO

FEEDBACK-DIRECTED OPTIMIZATION

FDO: PASS1= -Qprof_gen PASS2= -Qprof_use

BASE TUNING

C: -Qipo +FDO -QxK shlw32M.lib
 C++: -Qipo -QxK -GX -GR

PEAK TUNING

164.gzip: -Qipo +FDO -QxK shlw32M.lib
 175.vpr: -Qipo +FDO -QxK
 176.gcc: -Qipo +FDO -QxK shlw32M.lib
 181.mcf: -Qipo +FDO -QxK shlw32M.lib
 186.crafy: -Qipo +FDO -QxK shlw32M.lib
 197.parser: -Qipo +FDO -QxK shlw32M.lib
 252.eon: -Qipo +FDO -QxK
 253.perlbnk: -Qipo +FDO -QxK shlw32M.lib
 254.gap: -Qipo +FDO -QxK shlw32M.lib



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Notes/Tuning Information (Continued)

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255.vortex: -Qipo +FDO -QxK -Oa shlw32M.lib
256.bzip2:  -Qipo +FDO -QxK -Oa shlw32M.lib
300.twolf:  -Qipo +FDO -QxK -Oa shlw32M.lib
EXTRA LIBRARIES
shlw32M.lib: MicroQuill SmartHeap Library 5.0
              www.microquill.com
```